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Important Instructions to examiners:

- 1) The answers should be examined by key words and not as word-to-word as given in themodel answer scheme.
- 2) The model answer and the answer written by candidate may vary but the examiner may tryto assess the understanding level of the candidate.
- 3) The language errors such as grammatical, spelling errors should not be given more Importance (Not applicable for subject English and Communication Skills.
- 4) While assessing figures, examiner may give credit for principal components indicated in the figure. The figures drawn by candidate and model answer may vary. The examiner may give credit for any equivalent figure drawn.
- 5) Credits may be given step wise for numerical problems. In some cases, the assumed constantvalues may vary and there may be some difference in the candidate's answers and model answer.
- 6) In case of some questions credit may be given by judgement on part of examiner of relevant answer based on candidate's understanding.
- 7) For programming language papers, credit may be given to any other program based on equivalent concept.

Q. No.	Sub Q. N.		Answers	Marking Scheme
1	(A)	Attempt	any FIVE of the following:	10- Total Marks
	(a)	Define: (i) (ii)	Admittance Conductance	2M
	Ans:	It is given ii) Conducta	Admittance-: ce is defined as the reciprocal of impedance .It is denoted by Y. by Y = (1/Z) Conductance: ince is the ratio of resistance to the square of impedance .It is denoted by G. by G = (R/Z²)	1 M for each definitio n

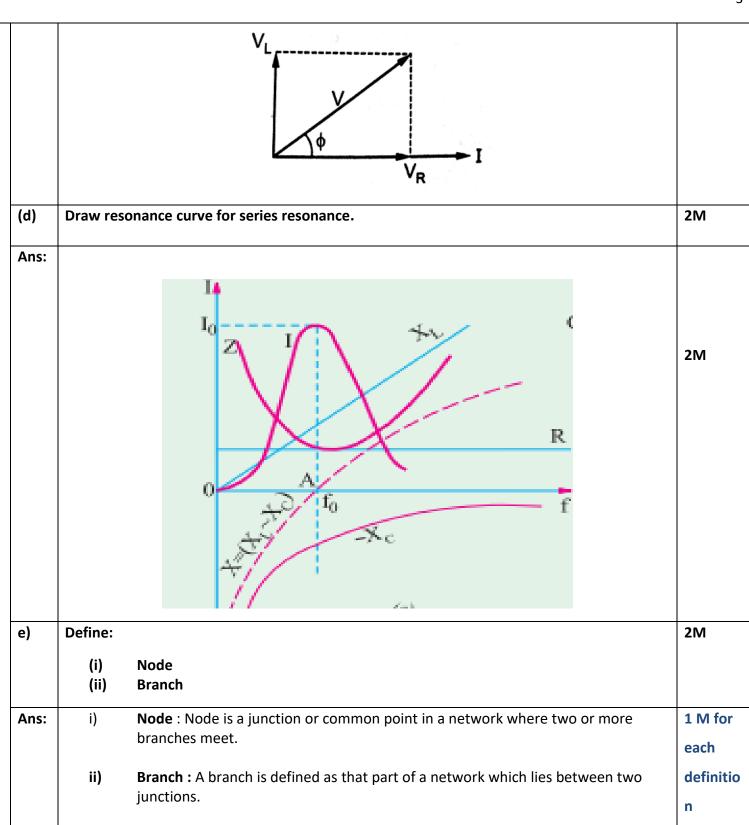
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(b)	Write the equation of open circuit Z parameter.	2M
Ans:		
	Solf - The equations for open circuit Z-parameters are	
	$Z_{11} = \frac{V_1}{I_1} \cdot (OhmA)$	
	71 72.50	½M fo
	$Z_{21} = \frac{V_2}{I_1} \Big _{I_{2}=0}$ (ohms)	each
		equation
	$Z_{12} = \frac{V_1}{I_2} \Big _{I_1 = 0} - (Ohms)$	n
	$Z_{22} = \frac{V_2}{J_2} \Big _{J_1 = 0}$ (ohms)	
(c)	Draw phasor diagram for R-L series circuit.	2M
Ans:	Phasor diagram for R-L series circuit.	
	Dl	
	Phasor diagram:	2M
	$ \swarrow_{\Phi} $	
	I I	

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f)	State Thevenins theorem.	2M
Ans:	Any network containing active and/or passive elements and one or more dependent and/or independent voltage/or current sources can be replaced by an equivalent network containing a voltage source (Thevenin's equivalent voltage V_{TH} or V_{OC}) and a series resistance (called Thevenin's equivalent resistance R_{TH}), where V_{OC} or V_{Th} is the voltage measured across specified open terminals and R_{Th} is the resistance measured across the same terminals when all the sources present in the network are replaced by their internal resistances.	2M
g)	Write the formula for Delta to Star conversion giving examples.	2M
Ans:	The formula for Delta to Star conversion-	2M
	V_1 R_c R_b V_1 R_1 R_2 R_3 V_3 R_3	

Q.	Sub	Answers	Marking
No.	Q. N.		Scheme
2		Attempt any THREE of the following:	12- Total Marks

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a)	For RLC series circuit draw voltage triangle, power triangle and impedance triangle along with proper labellings and equations for condition $V_L > V_C$.	4M
Ans:		Voltag
		triang
		1 M
	Solm -> For R-L-C series circuit (For cond" VL7 Vc)	
	i) Voltage Triangle is given by	Imped
	V AS	nce
	V P P P P P P P P P P P P P P P P P P P	triangl
		1M
	ii) Impedence Triangle for VL>Vc is given by	Power
	121	triang
	121 Power Triangle for VL/VC is given by F=VJcoso P=Active Power	-1 M
	iii) Power Triangle for VL) VC is given by	
	7.3/1 17.01 A 17.01 A 2 3 3	Voltag
	BY OR MAN OR MAN SE	equati
		n-1/2ľ
	iv) The vtg. and current equations for VL>VC (x(t) = Vm simut	Currei
	u(t) = Vmsimut	equati
	$i(t) = Imsin(\omega t - \phi)$	n-1/2
b)	Define and state equations for (i) Active Power (ii) Reactive Power	4M
	(iii) Apparent Power.	
Ans:	i)Active Power (P):	Active
		power
	The active power is defined as the average power P _{avg} taken by or consumed by the given	1.5 M

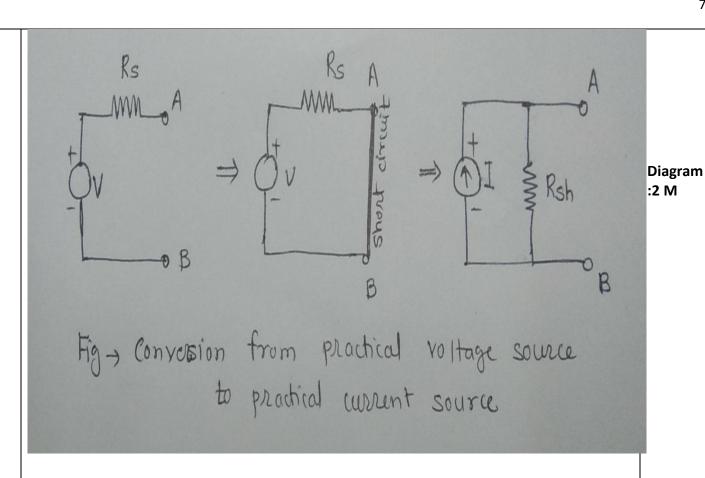
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Ans:		
c)	Explain the steps for converting practical voltage source into practical current source.	4M
	J-VI-1 2 VA	
	S=VI=I ² Z VA	
	Unit: volt-ampere (VA) OR kilo-volt-ampere (kVA) OR Mega-volt-ampere (MVA)	
	It is the product of rms values of applied voltage and current.	
	iii)Apparent Power (S):-	
	Units: - VAR OR KVAR	
	Q= V.I. sinØ	
		1 M
	The reactive power is defined as the product of V, I and sine of angle between V and I.	t pov
	It is the power developed in the reactive elements present in the circuit. (OR)	Appa
	ii)Reactive Power (Q):-	
	Unit: - Watt OR Kilowatt	
	$P = V.I.CosØ = I^2R$	
	It is the power which is actually dissipated in the circuit resistance.	1.5 N
	(OR)	Reac

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A given voltage source with a series resistance can be converted into an equivalent current source with a parallel resistance.

The steps for converting practical voltage source into practical current source are:

i) Terminals A and B of the given voltage source is short circuited as shown. Current supplied by the source is given by

Current I=V/R.

ii)The value of resistance which is connected in parallel with the equivalent current source will have the same value as that of series resistance. That is Rs=Rsh.

ii)The equivalent current source is connected in parallel with the shunt(parallel) resistance.

Steps

2 M

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	Three resistances 32 Ω , 40 Ω , 48 Ω are connected in star circuit. Determine its equivalent delta circuit.	4M
ns:		
	$R = R_1 R_2 + R_2 R_3 + R_3 R_1$ $= (32 \times 40) + (40 \times 48) + (48 \times 32)$ $= R_1 R_2 + R_2 R_3 + R_3 R_1$ $= (32 \times 40) + (40 \times 48) + (48 \times 32)$ $= R_1 R_2 + R_2 R_3 + R_3 R_1$ $= R_1 R_2 + R_2 R_3 + R_3 R_1$ $= R_1 R_2 + R_2 R_3 + R_3 R_1$ $= R_1 R_2 + R_2 R_3 + R_3 R_1$ $= R_1 R_2 + R_2 R_3 + R_3 R_1$ $= R_1 R_2 + R_2 R_3 + R_3 R_1$ $= R_1 R_2 + R_2 R_3 + R_3 R_1$ $= R_1 R_2 + R_3 R_1$ $= R_1 R_3 + R_3 R_2$ $= R_1 $	
	$R_{23} = \frac{4736}{R_3} = \frac{4736}{48} = 98.66 2$	
	$R_{23} = \frac{R}{R_1} = \frac{4736}{32} = 1482$	
	$R_{31} = \frac{R}{R_2} = \frac{4736}{40} = 118.4 2$	

Q.	Sub	Answers	Marking
No.	Q. N.		Scheme
3		Attempt any THREE of the following :	12- Total Marks

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(

a)	If $Z_1 = 3 + j7$ and $Z_2 = 12 - j16$ are connected in parallel. Find the equivalent impedance of combination.	4M
Ans:	Equivalent impedance, $Z_{eq} = \frac{Z_1 Z_2}{Z_1 + Z_2} = \frac{(3+j7)(12-j16)}{(3+j7) + (12-j16)} = \frac{(7.62 < 66.8)(20 < -53.13)}{(7.62 < 66.8) + (20 < -53.13)} =$	Formula 1M
	Z_1+Z_2 $(3+j7)+(12-j16)$ $(7.62<66.8)+(20<-53.13)$	Steps 1M
	$= \frac{152.4 < 13.67}{17.5 < -40} = 8.71 < 53.67 = 5.16 + j7 \Omega$	Ans. 2N
b)	Determine Bandwidth and Quality factor (Q) for the series circuit.	4M
Ans:		2 M
	Band width:	each
	The bandwidth of aseries circuit is given by the band of frequencies which lies between two points on either side of f_0 where current falls to $I_0/\sqrt{2}$. (graph may be desirable)	
	$I_0 = \frac{V}{R}$ $\frac{I_0}{\sqrt{2}}$ $O \qquad f_1 \qquad f_0 \qquad f_2 \qquad f$	
	From the given fig., band width AB is, $AB = \Delta f = f_2 - f_1 \text{ or } AB = \Delta \omega = \omega_2 - \omega_1 \text{ where } f_1 \text{ and } f_2 \text{ are the corner or edge frequencies.}$	

(OR)

$$\Delta f = f_r/Q_r = \frac{\frac{1}{2\pi\sqrt{LC}}}{\frac{1\sqrt{L}}{R\sqrt{C}}} = \frac{R}{2\pi L}$$

Quality factor:

Note: any one of the following can be considered

Reciprocal of power factor is called quality (*Q*) factor or its figure of merit. The *Q*-factor of aseries circuit can be defined as the ratio of impedance to resistance.

$$Q = \frac{Z}{R}$$

It is also defined as,

$$Q = 2\pi \frac{\text{maximum energy stored per cycle}}{\text{Energy dissipated per cycle}}$$

For a resonant circuit it may be determined in any of the following ways

i) It is given by the voltage magnification produced in the circuit at resonance.

Voltage magnification
$$=\frac{V_{L_0}}{V}=\frac{I_0X_{L_0}}{I_0R}=\frac{\text{reactive power}}{\text{active power}}=\frac{X_{L_0}}{R}=\frac{\omega_0L}{R}=\frac{\text{reactance}}{\text{resistance}}$$
 or $=\frac{V_{C0}}{V}=\frac{I_0X_{C0}}{I_0R}=\frac{\text{reactive power}}{\text{active power}}=\frac{X_{C0}}{R}=\frac{\text{reactance}}{\text{resistance}}=\frac{1}{\omega_0\ CR}$ $\therefore \ \mathcal{Q}-\text{factor}, \qquad \mathcal{Q}_0=\frac{\omega_0\ L}{R}=\frac{2\pi\ f_0\ L}{R}=\tan\phi$

ii)

$$Q\text{-factor} = 2\pi \frac{\text{maximum stored energy}}{\text{energy dissipated per cycle}} \qquad \dots \text{in the circuit}$$

$$= 2\pi \frac{\frac{1}{2}LI_0^2}{I^2 R T_0} = 2\pi \frac{\frac{1}{2}L(\sqrt{2}I)^2}{I^2 R (1/f_0)} = \frac{I^2 2\pi f_0 L}{I^2 R} = \frac{\omega_0 L}{R} = \frac{1}{\omega_0 CR} \qquad \dots (T_0 = 1/f_0)$$

iii)

resonant frequency,
$$f_0 = \frac{1}{2\pi \sqrt{(LC)}}$$
 or $2\pi f_0 = \frac{1}{\sqrt{(LC)}}$

Substituting the above in equation, $Q_0=\frac{2\pi f_0 L}{R}$, $\,$ we get,

$$Q_0 = \frac{1}{R} \sqrt{\frac{L}{C}}$$

iv) Q-factor of a resonant series circuit may be written as,

$$Q_0 = \frac{\omega_0}{\text{bandwidth}} = \frac{\omega_0}{\Delta \omega} = \frac{\omega_0}{R/L} = \frac{\omega_0 L}{R} = \frac{L}{R\sqrt{LC}} = \frac{1}{R}\sqrt{\frac{L}{C}}$$

v) It may also be deduced as,

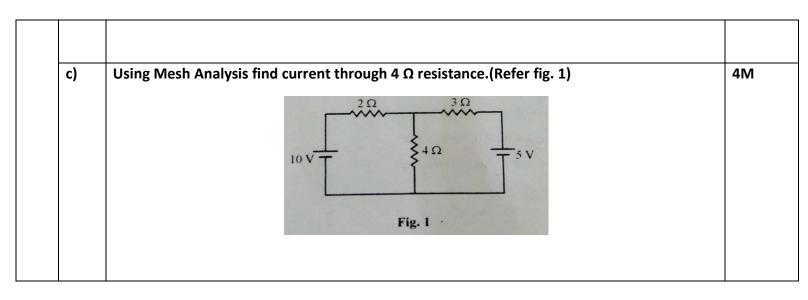
$$Q_0 = \frac{\omega_0 L}{R} = \frac{1}{\omega_0 C R} = \frac{1}{R} \sqrt{\frac{L}{C}} = \sqrt{\frac{X_{L0} X_{C0}}{R}} = \frac{f_0}{B_{hp}} = \frac{\omega_0}{\omega_2 - \omega_1} = \frac{f_0}{f_2 - f_1}$$

Where B_{hp}= bandwidth of the circuit



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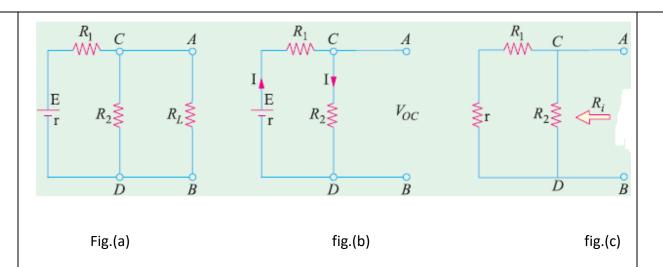
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Ans:	24 , 34	Correct
	A 25 B 352	calculat
	10VT) }4-27 + 5V	on-
	1, 4 12	4M(Giv
	f E	step
	1 Apply KUL to loop NBEFA,	marking
)
	$-2I_{1}-4(I_{1}-I_{2})+10=0$	
	$-2\widehat{I}_{1}-4\widehat{I}_{1}+4\widehat{I}_{2}=-10$	
	- 6I, + 4Î, = -10 - O	
	@ Apply KUL to loop BCDEB,	
	$-3\tilde{I}_{2}-5-4(\tilde{I}_{2}-\tilde{I}_{1})=0$	
	$-3I_{0}-5-4I_{2}+4I_{1}=0$	
	a = a = a = a = a = a = a = a = a = a =	
	Solving eque of & 2 ele get,	
	G 1-92A 72= 0-38 A	
	$\widehat{I}_1 = \widehat{I}_2 = 1.92 - 0.38$	
	Lower through 402 , $1 = 1.92 - 0.38$ Convent through 402 , $1 = 1.92 - 0.38$ $= 1.54 A$ (downwoods)	
d)	Explain the procedure for solving Thevenins theorem using suitable example.	4M
Ans:	Steps to find Thevenin's equivalent circuit, taking an example is as follows:	1M eac

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- 1. From the given circuit(fig.a), Remove R_L from the terminals A and B and redraw the circuit as shown in Fig.b.
- 2. Calculate the open-circuit voltage Voc which appears across terminals A and B.

As seen, $Voc = \text{drop across } R_2 = IR_2 \text{ where } I \text{ is the circuit currentwhen } A \text{ and } B \text{ are open.}$

$$I = \frac{E}{R_1 + R_2 + r}$$
 \therefore $V_{oc} = IR_2 = \frac{ER_2}{R_1 + R_2 + r}$ [r is the internal resistance of battery]

It is also called 'Thevenin voltage' V_{th} .

3.Now, imagine the battery to be removed from the circuit, leaving its internal resistance *r* behind and redraw the circuit, as shown in Fig.(*c*). When viewed *inwards* from terminals *A* and *B*, the equivalent resistance is given as,

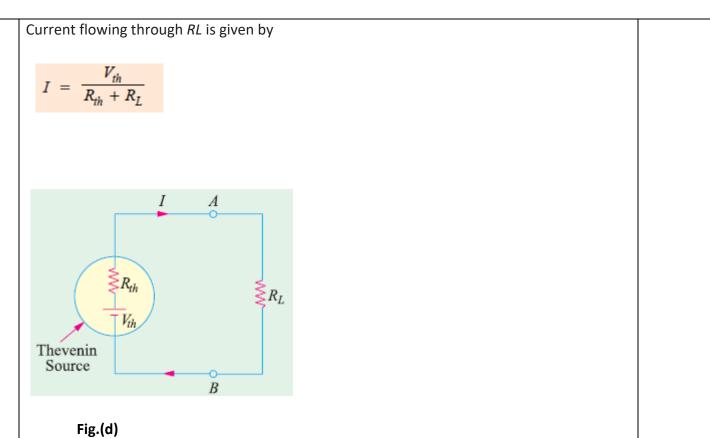
$$R = R_2 || (R_1 + r) = \frac{R_2(R_1 + r)}{R_2 + (R_1 + r)}$$

This is called Thevenin's equivalent resistance R_{th.}

4. Connect R_L back across terminals A and B(fig.d)from where it was temporarily removed earlier.

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Q. No.	Sub Q. N.	Answers	Marking Scheme
4		Attempt any THREE of the following :	12- Total Marks
	(a)	A coil has resistance of 4 Ω and an inductance of 9.55 mH. Calculate (i) Reactance (ii) The impedance (iii) The current taken from 240 V, 50 Hz supply.	4M

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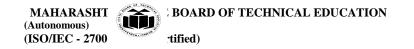
Ans:	452 L= 9.53 MH	1M ea
		param
	240V, 50Hz	ers &
	· ·	1M fo
	Data given: R=42, L= 9.55 mA, V=240V, f=50H3	prope
	$R = 4\sqrt{L}$, $L = 1/33$ $R(R)$, $V = R(R)$	steps
	i) Reacted Ce, XL = 271fL = 2x3.14x50x9.55x10 = 312	follow
	i) Reactance, $XL = 271fL = 2x3.14 \times 50 \times 9.55 \times 10^3 = 3.02$ ii) Impedance, $Z = R+jXL = 4+j3 = 5/36.87.02$	
	iii) Connent, $I = \frac{V}{Z} = \frac{240}{5} = \frac{48A}{5}$	
	(or) = ===	
	(i) Impedance, $7 = \sqrt{R^2 + \chi L^2} = \sqrt{4^2 + 3^2} = 5.02$	
	") $3Mpc$ $\frac{1}{4} = \sqrt{R^2 + 3} = \frac{3.02}{2}$	
(b)	Draw the phasor diagrams for a series RL and series RC with AC supply.	4M
Ans:	Phasor diagram of RL series circuit:	
Alis.	Thasor diagram of the series circuit.	2M fo
Alis.		2M fo
Alis.	B A	each
Alis.		2M fo each diagra
Alis.	V $V_{ m L}$	each
Alis.	V	each

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Phasor diagram of RC series circuit: $-V_C$ Compare series and parallel circuits. 4M (c) Ans: 1M each for any 4 Sr.No. **Series circuit Parallel circuit** points Equi-potential ends are connected together 1 Element are connected end-onend R_2 R_3 $\geq R_2$ Same current flows through all 2 Same voltage acts across all parts of the parts of the circuit. circuit 3 Different resistors have their Different resistors have their individual individual voltage drops. currents. Voltage drops are additive. 4 Branch currents are additive.



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5	Applied voltage equals the sum of different voltage drops.	Individual voltages is equal to supply voltage
6	Resistances are additive.	Conductances are additive.
7	Powers are additive.	Powers are additive
	2Ω ¥ 1	Α
	10 V_T	В

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Ans:	a in a hocemen	Correct
	Adding both the Someces the Circuit becomes,	calculati
	4.2 T	on-4M
	m to	
	201 -	
	Its equivalent Consumt Source is	
	Į	
	$5A^{\uparrow}$ A^{2} $T=5A$	
	•	
(e)	Using super-position theorem find current through 4 Ω resistance.(Refer fig. 3)	4M
	$\frac{3\Omega}{\sqrt{2}}$	
	Text and the Property of the P	
	$6V + 4\Omega $ $+ 12V$	
	2Ω	
	Fig. 3	

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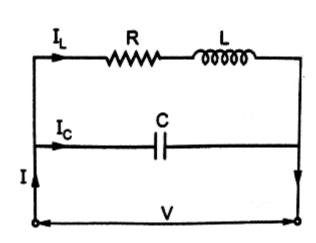
Ans:	$\frac{3 \text{top } T \text{ (With 6V Source alone)}}{I' \frac{3 \cdot 2}{M} \frac{5 \cdot 2}{M} \frac{I'_2}{I'_2}} \qquad T' = \frac{V}{Req} = \frac{6}{3 + 4 (9+2)}$ $= \frac{6}{3 + 2 \cdot 55} = 1 \cdot 08 \text{ A}$	Corrct calculati on -4M
	$I_1' = I \times (5+2) = \frac{1 \cdot 08 \times 7}{11} = \frac{0.694}{11}$	
	$ \frac{gtap II}{VI''} (Wilt 12V a lone) $ $ \frac{J_{2}^{"} = V}{Req} = \frac{12}{5 + (3 4) + 2} $ $ = \frac{12}{8 \cdot 71} = 1 \cdot 38A $	
	$I_1'' = \underbrace{\hat{I}_2'' \times 3}_{3+4} = \underbrace{1.38 \times 3}_{7} = 0.59 \text{ A} \downarrow$ Consert through $402 = \hat{I}_1' + \hat{I}_1'' = 0.69 + 0.59 = 1.284 \downarrow$	

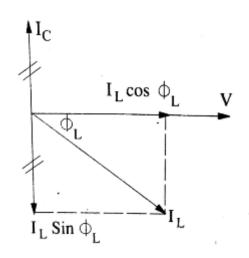
Q. No.	Sub Q. N.	Answers	Marking Scheme
5.		Attempt any TWO of the following:	12- Total Marks
	a)	Derive the expression for resonance frequency for parallel circuit.	6M
	Ans:	A parallel circuit containing an inductance and a capacitance is said to be in resonance when the current through the parallel combination is in phase with the supply voltage.	Diagram 2M,
		Consider a parallel combination of L and C as shown below.	derivati on 4M

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The vector diagram for this circuit is as shown.

The net reactive component of current = $Ic - I_LSin\phi_L$.

At resonance ,its value is zero.

That is $I_c - I_L Sin \phi_L = 0$ or $I_c = I_L Sin \phi_L$

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From the diagram above, $I_1 = V/L$	From	the diagram above,	$I_1 = V/Z$
-------------------------------------	------	--------------------	-------------

$$Sin\phi_L = X_L/Z$$
, $I_c = V/X_c$

Substituting these values in the above equation, the condition for resonance becomes

$$V/X_c = (V/Z)(X_L/Z)$$
 or $(X_L)(X_c) = Z^2$

Substituting $X_L = \omega L$ and $X_c = 1/\omega C$

$$(\omega L/\omega C) = Z^2$$

$$L/C = R^2 + (2\pi f L)^2$$

$$(2\pi fL)^2 = \frac{L}{C} - \frac{R^2}{L^2}$$

$$2\pi f = \sqrt{\frac{1}{LC} - \frac{R^2}{L^2}}$$

If $f=f_r = resonant$ frequency, then,

$$f_r = \frac{1}{2\pi} \sqrt{\frac{1}{LC} - \frac{R^2}{L^2}}$$

If R is negligible, then

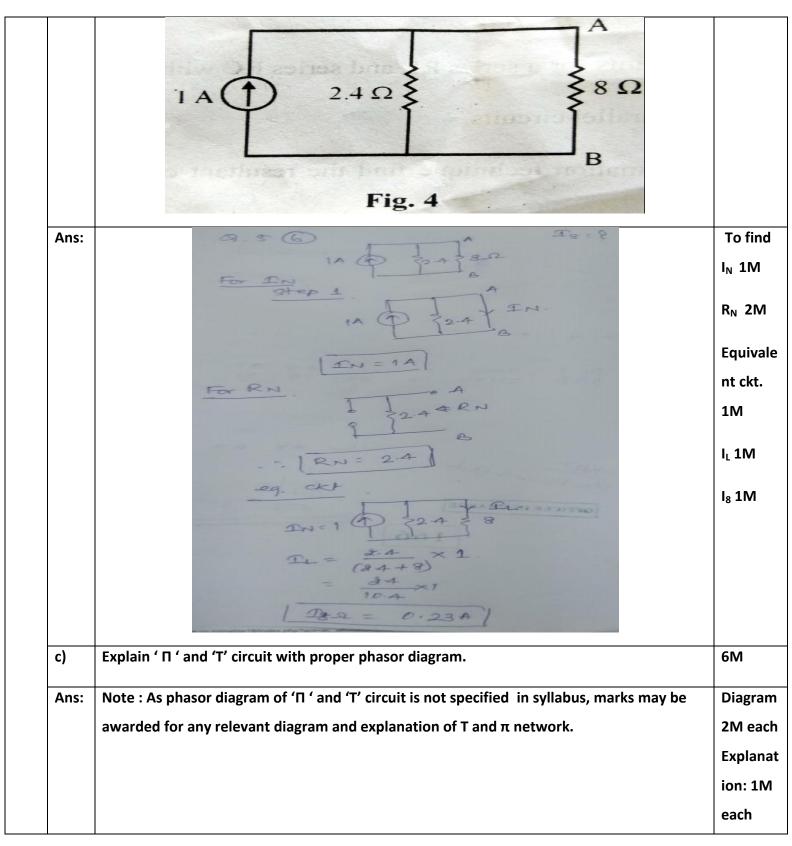
$$fr = \frac{1}{2\pi\sqrt{LC}}$$

b) Calculate current through 8 Ω resistance using Norton's theorem.(Refer fig. 4)

6M

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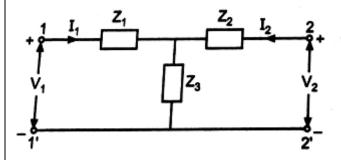
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T networks are used to represent the equivalent of transmission line theory, filters, etc.

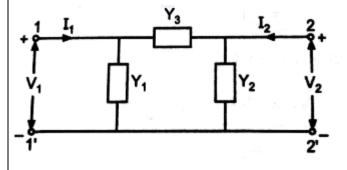
Ladder network in transmission lines is constructed using T network in series. If Z parameters and ABCD parameters of a network are known, then T network can be constructed.

T network



π network

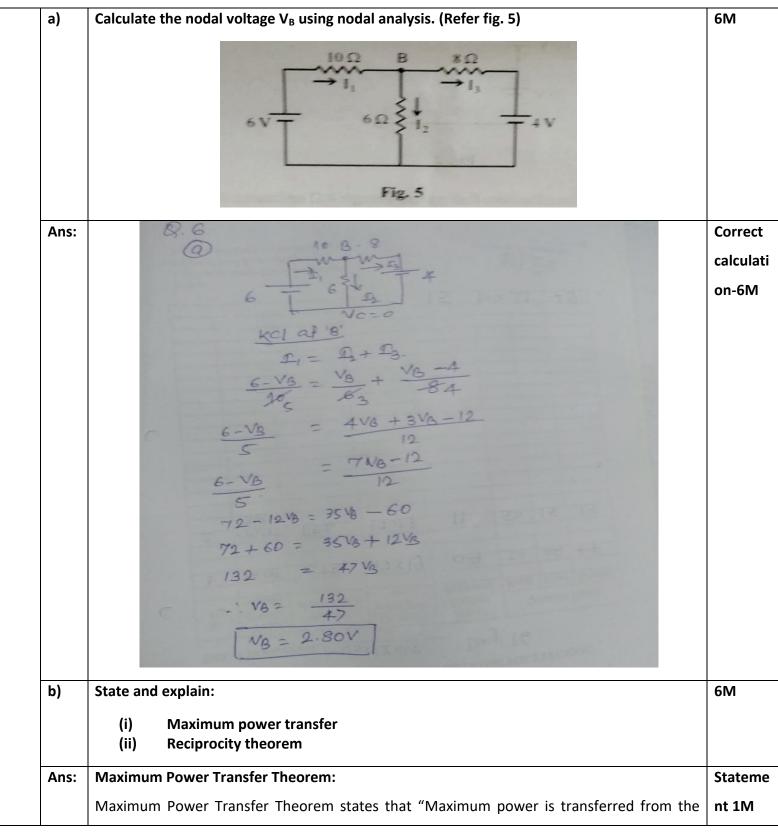
This is also an important network frequently used in transmission line theory. If Y parameters of a network are known, then π network can be constructed.



Q.	Sub	Answers	Marking
No.	Q. N.		Scheme
6.		Attempt any TWO of the following :	12- Total Marks

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source to the load when the load resistance is equal to the Thevenin's equivalent resistance of the given circuit as seen from load terminals"

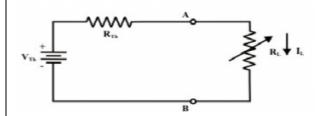
Explanat

ion

each

2M each

.i. e,
$$R_1 = R_{TH}$$



In above figure a variable load resistance R_L is connected to an equivalent Thevenin circuit of original circuit. The current for any value of load resistance is,

$$I_L = \frac{V_{TH}}{R_{TH} + R_L}$$

Then, the power delivered to the load is..

$$P_L = I_L^2 \times R_L$$
. $P_L = \left(\frac{V_{TH}}{R_{TH} + R_L}\right)_L^2 \times R_L$

Maximum power transfer occurs when the load resistance $R_L = R_{TH}$.

Substituting R_L= R_{TH}in the above equation, we get

$$P_L = [V_{Th}/(R_L + R_L)^2] R_L$$

$$= (V_{Th}/2R_L^2) \times R_L$$

$$\mathsf{P}_{\mathsf{Lmax}} = \frac{V^2}{4R_L}$$

Reciprocity Theorem:

Statement:

In any linear bilateral network, if a source of emf E in any branch produces a current I in any other branch , then the same emf E acting in the second branch would produce the same current I in the first branch.

(OR)

c)

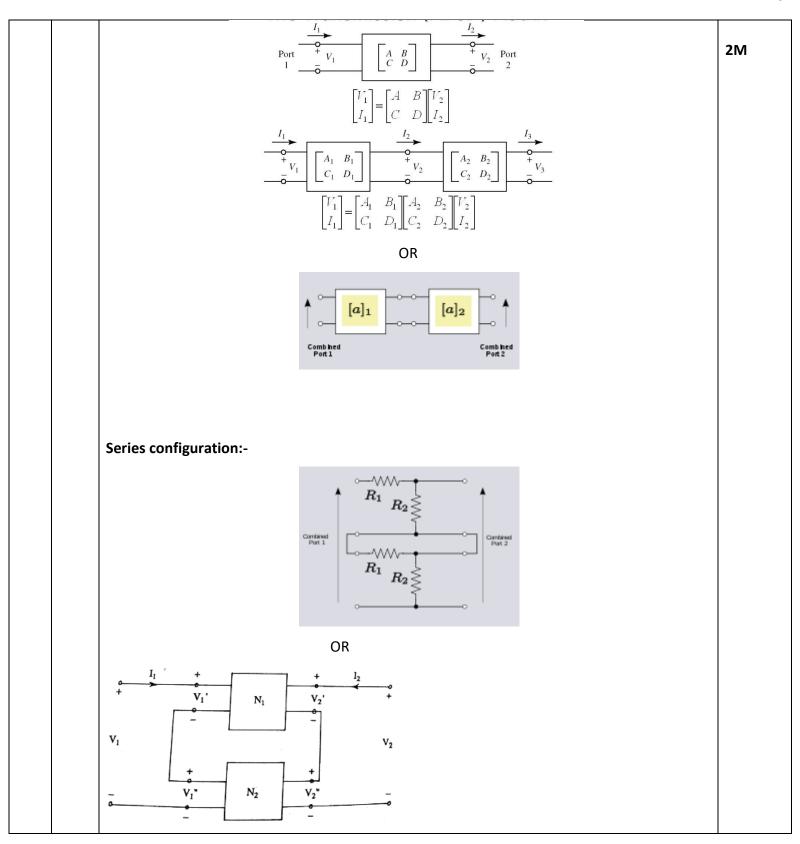
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27 In any branch of a network or circuit, the current due to a single source of voltage (V) in the network is equal to the current through that branch in which the source was originally placed when the source is again put in the branch in which the current was originally obtained. Explanation: Consider the two circuits shown below. The various resistances R₁, R₂, R₃ are connected in the circuit diagram above with a voltage source (V) in first loop and an ammeter in second loop in first circuit. In the second circuit the positions of voltage source and ammeter are interchanged According to Reciprocity Theorem, the ratio of V / I called transfer resistance. It remains same in both cases. In this way the theorem is useful for solving networks. Explain significance of two-port network. Also draw two port network for **6M** (i) Cascade configuration ABCD parameter (ii) Series configuration Significance of two-port network:-2M Ans: A two-port network is regarded as a "black box" with its properties specified by a matrix of numbers. This allows the response of the network to signals applied to the ports to be 2M calculated easily, without solving for all the internal voltages and currents in the network. Cascade configuration ABCD parameter :-

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Where N ₁ and N ₂ are two 2-port networks	